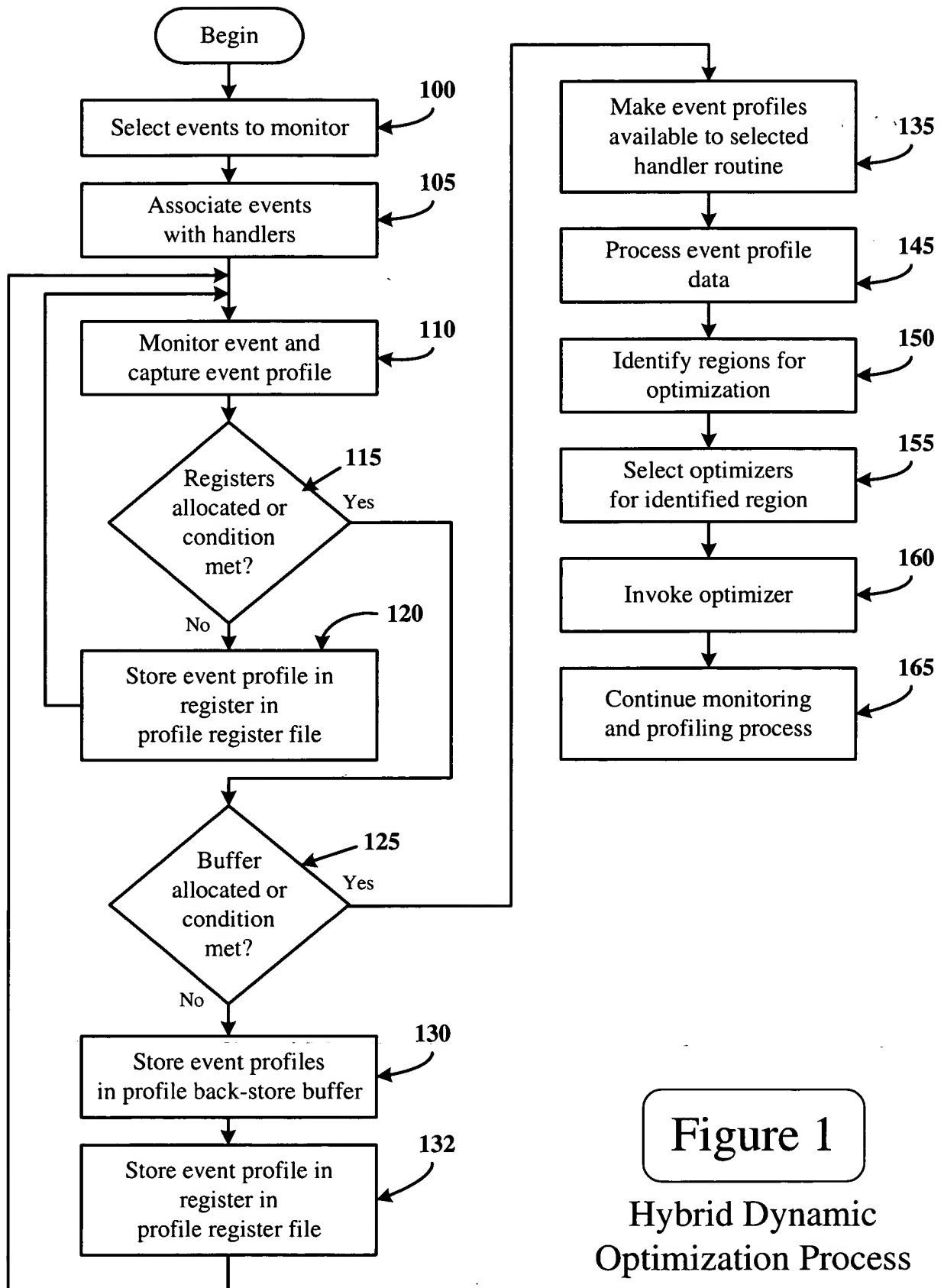


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**Figure 1**  
Hybrid Dynamic  
Optimization Process

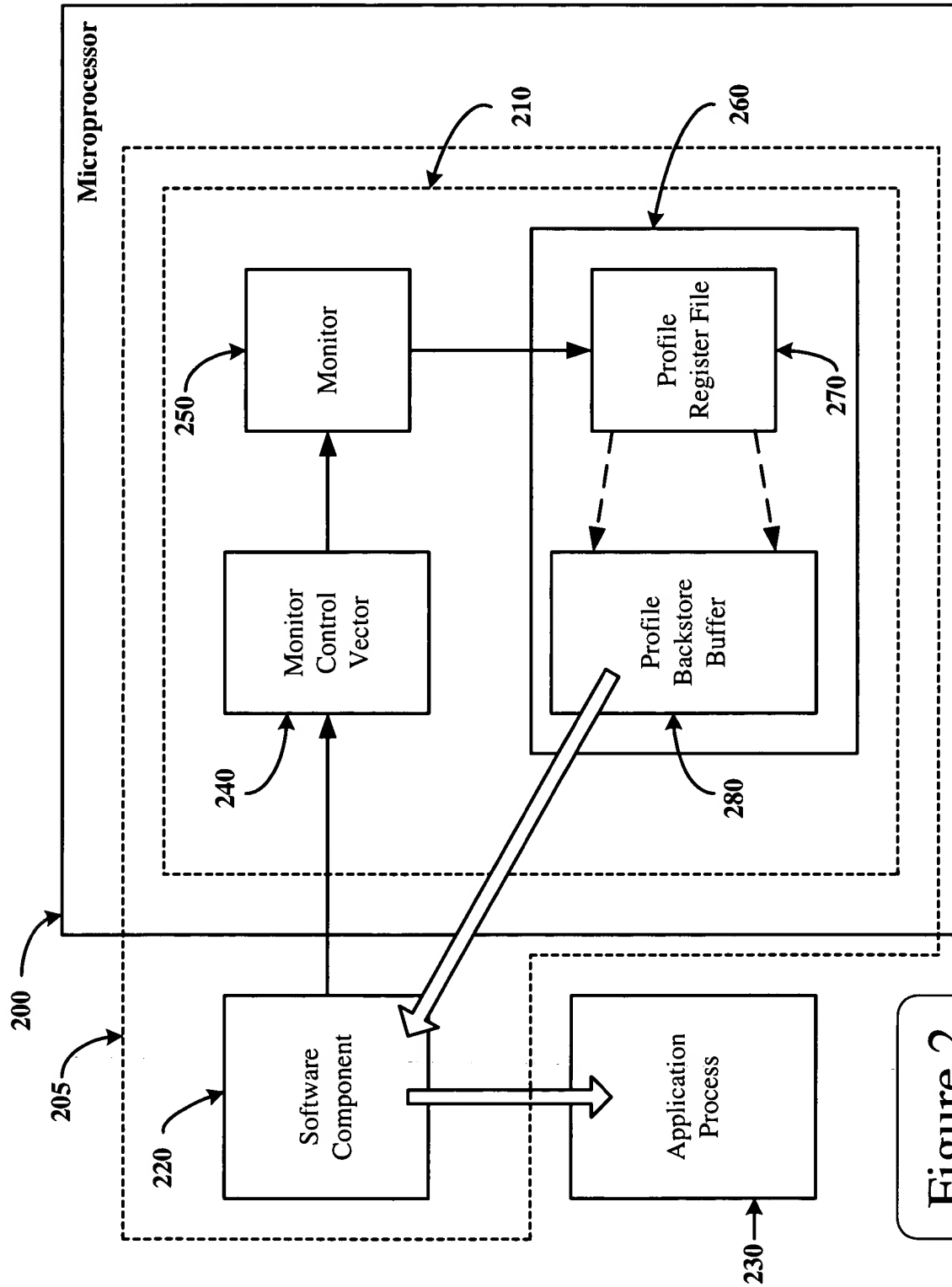


Figure 2

Hybrid Dynamic Optimizer

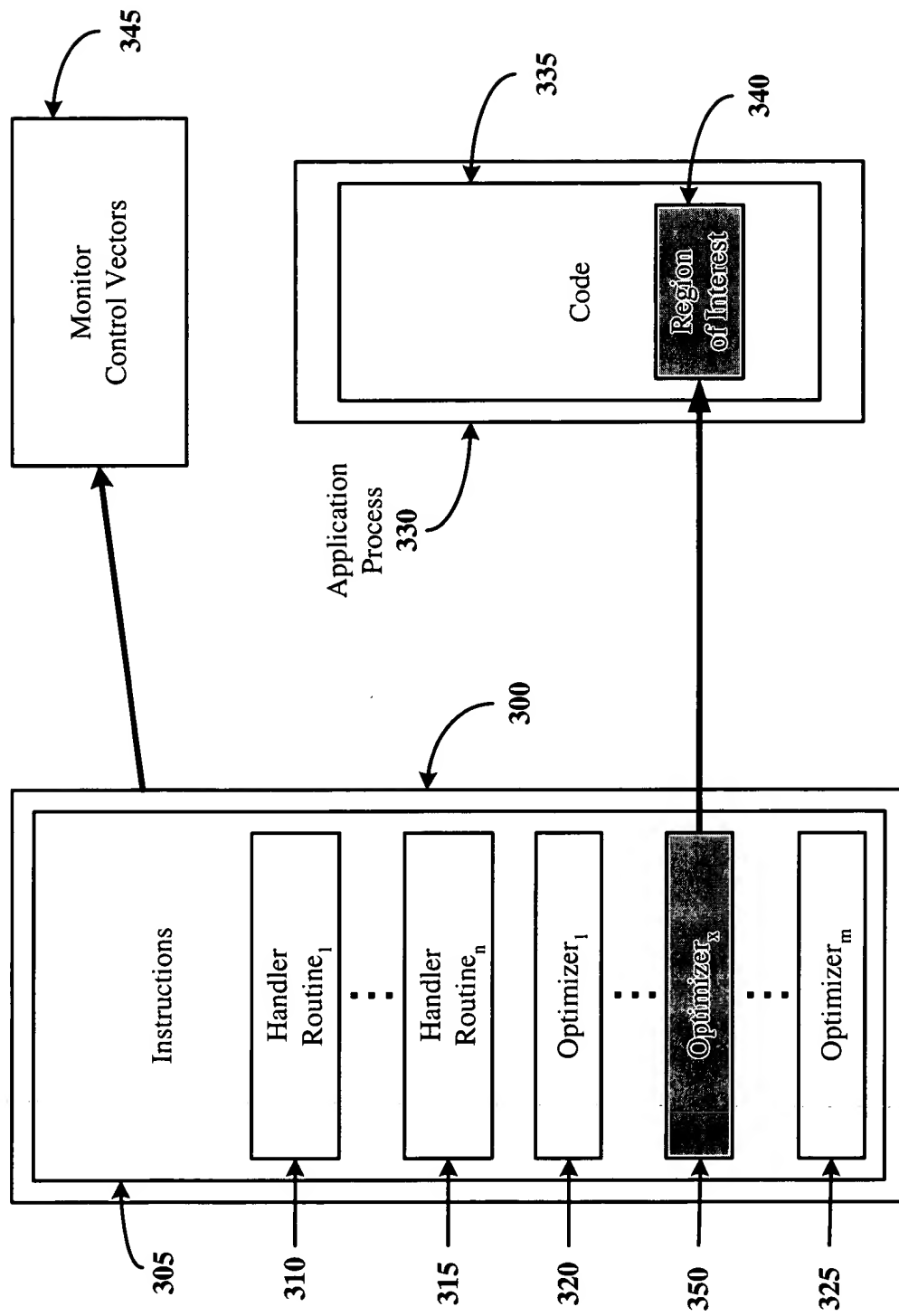


Figure 3

Software  
Component

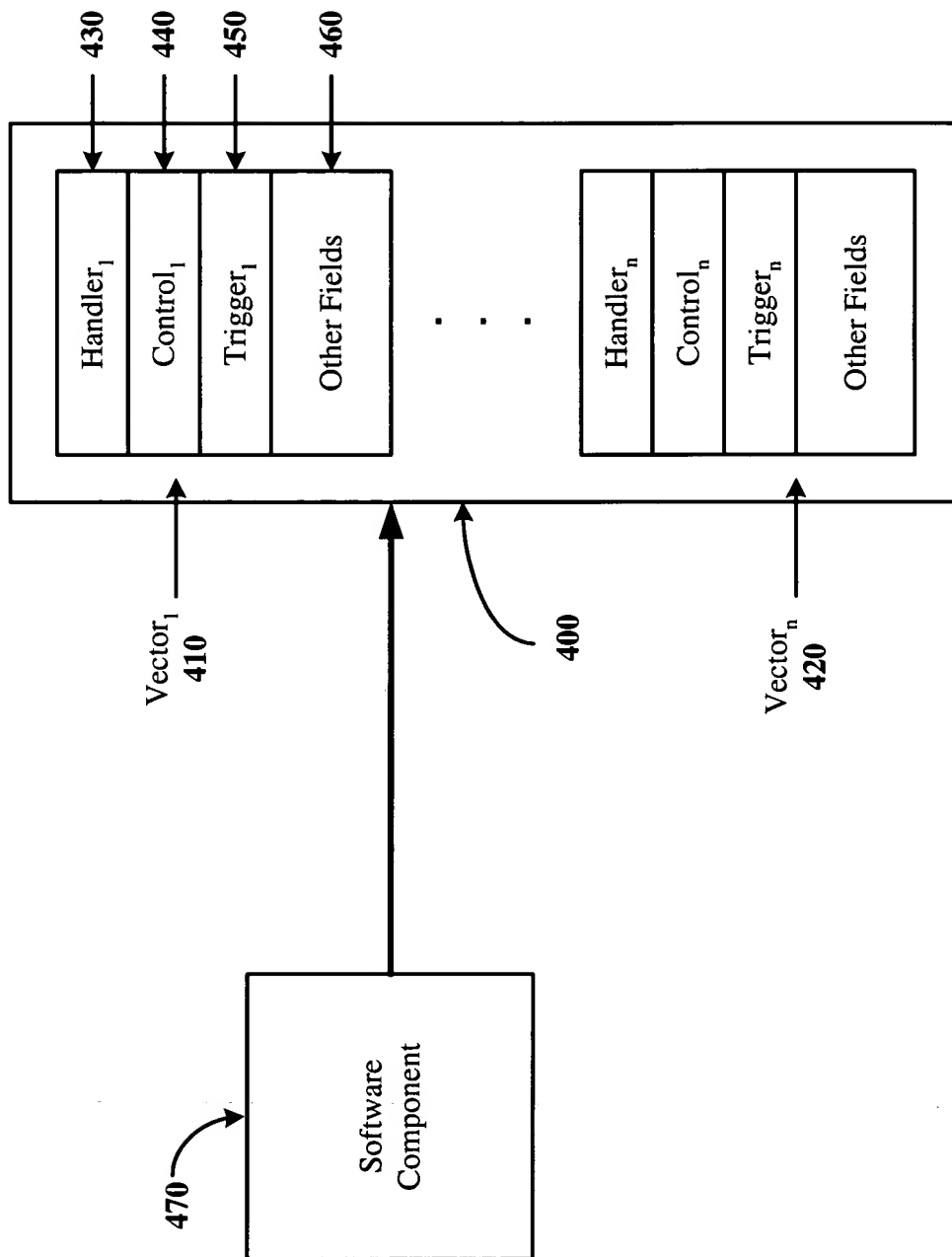


Figure 4

Monitor Control Vectors

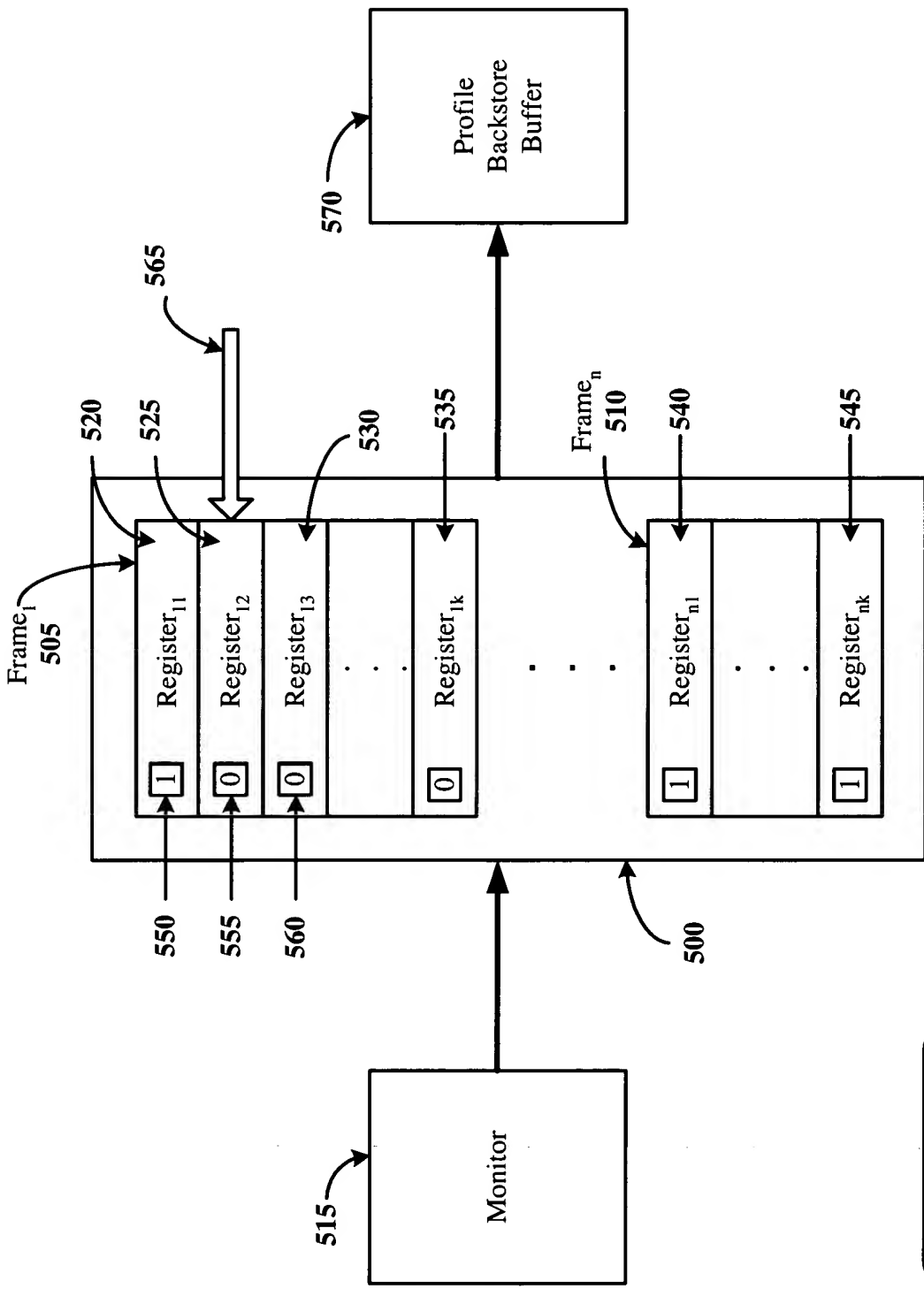
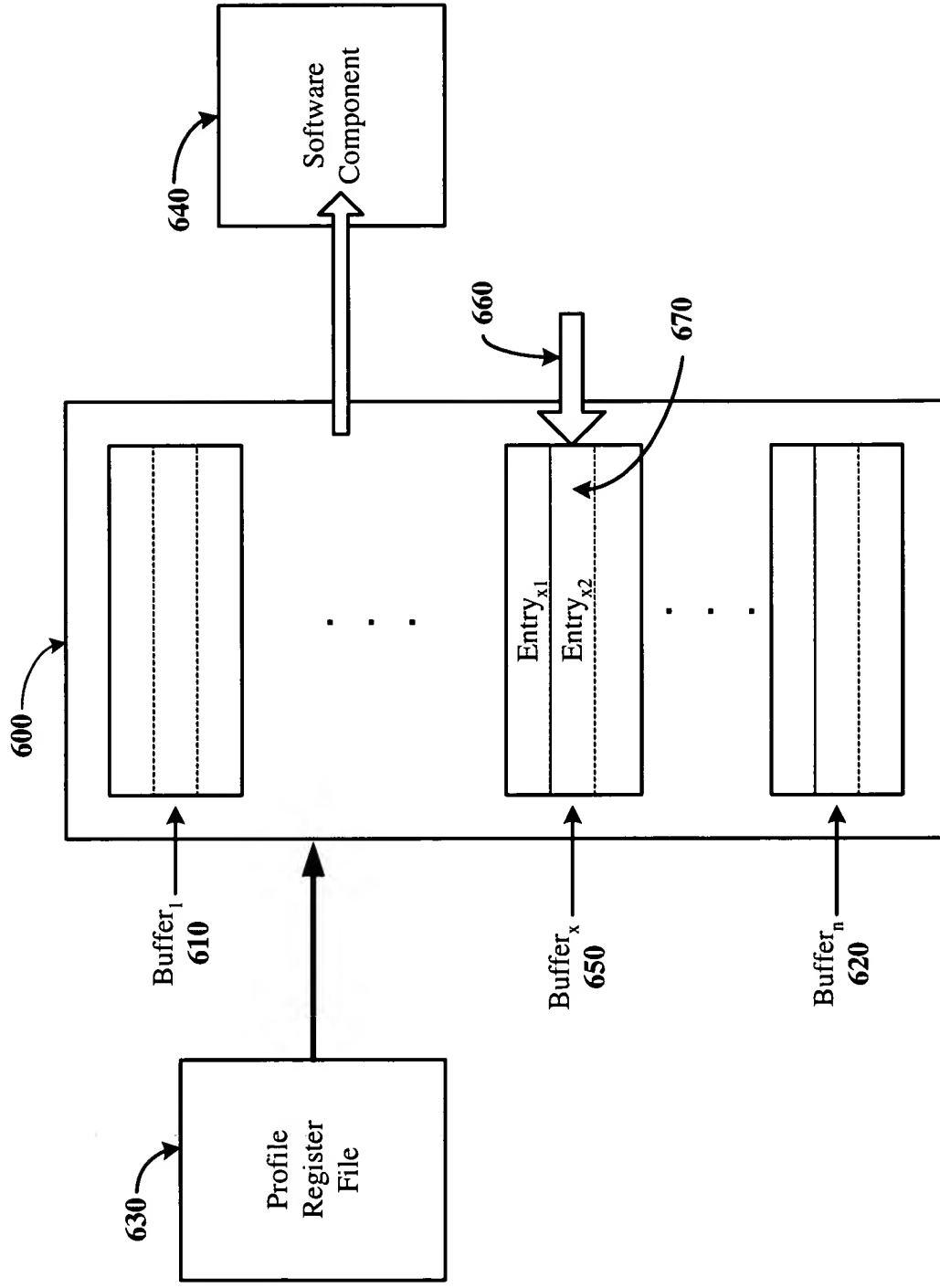


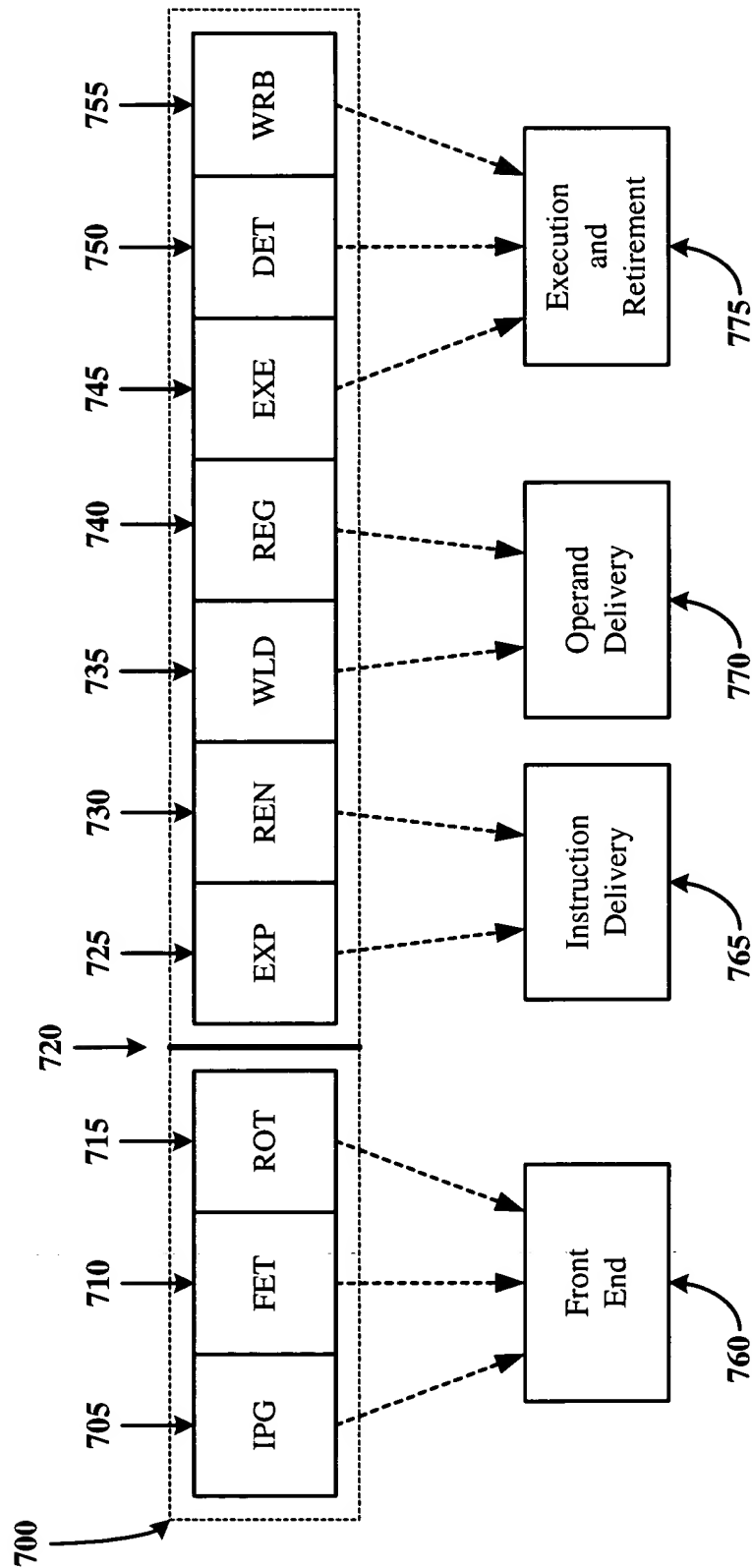
Figure 5

Profile Register File



Profile Backstore Buffer

Figure 6



Microprocessor Pipeline

Figure 7